ANALOG PULSE TRANSDUCER

VFTP2 -

Use

Inputs a DC signal of electric power or a current transducer, and converts the signal into a pulse of proportional frequency.



VFTP2 -915 (80 × 50 × 133mm/400g)

Features

- 1. Either one of open collector output, voltage output and contact output is selectable as output signal method. In addition, in the case of AC auxiliary supply and open collector output, product equipped with a power source for external relay's (DC24V, 40mA MAX) is manufacturable.
- 2. Function of cutting output against low input 1-10%.
- 3. Impulse with stands voltage 5 kV, $1.2/50 \mu s$ (between electric circuit and earth) positive/ negative polarity 3 times each is guaranteed.

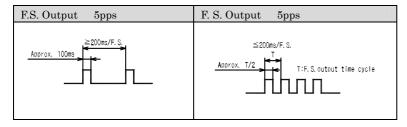
Specification

Kind of input (input resistance)	Output signal method	Auxiliary supply	Common specification
1: 0-1V (approx.1MΩ) 2: 0-5V (approx.1MΩ) 3: 0-10V (approx.1MΩ) 4: 1-5V (approx.10ΩΩ) 5: 0-1mA (approx.100Ω) 6: 0-5mA (approx.100Ω) 7: 0-16mA (approx.100Ω) 8: 0-16mA (approx.100Ω) 9: 4-20mA (approx.100Ω) 0: other than those above	☐: Voltage pulse 10Vp (load 2k) ②: Tr. open collector(O.C.) DC48V, 100mA ③: 1a contact (non-voltage contact) DC30V,2A ④: With power source for external relay. Tr. open collector(O.C.) DC24V, 40mA MAX, in the case of AC auxiliary supply only,	☐: AC100V+10%, -15%	Tolerance: ±0.25% Response time: 100ms + 1 Output frequency Consumption VA: AC power source:2.5VA DC power source:3W With power source for external relay AC power source:3.5VA
			Weight: AC power source:500g DC power source:400g

Output pulse number

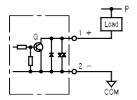
F.S output of transistor open collector output, voltage pulse output is 0.004306-277.8pps; F.S output of 1a contact output, transistor open collector output with power source for external relay is 0.00001667-1.000pps.

Output pulse width



Block diagram according to output

At the time of Tr. open collector output

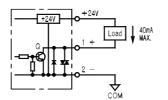


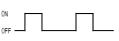


Load operates when internal transistor \mathbf{Q} is \mathbf{ON} .

Switching capacity of internal transistor Q is maximum DC48V, 100mA. Do not apply a reverse polarity voltage.

At the time of Tr. open collector output with power supply for external relay.

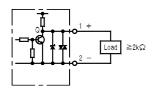




Load operates when internal transistor Q is ON.

With power source for external relay, DC24V, 40 mAMAX

At the time of voltage pulse output

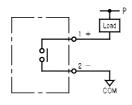




H...10V±10% L... 0.4V

Make external load more than or equal to $2k\Omega$.

At the time of 1a contact output

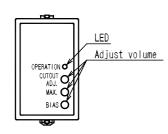




Load operates when contact is ON.

Contact capacity AC120V, 1A (load resistance) DC30V, 2A (Resistance load) Electrical life: 500,000 times.

Block diagram of front panel



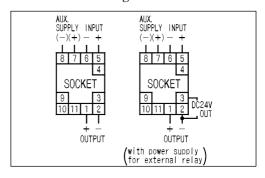
Output cut against low input Adjusts output to zero with CUTOUT ADJ/VR against 1-10% of rated input.

OPERATION LED

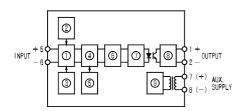
LED turns off during output cut against low input. LED turns on during pulse output. (Green)

It turns on when there is an input which value is greater than low input cut value.

Connection diagram



Block diagram



Scaling Externa

External span VR

External bias VR

VIF conversion

Cutout ADJ.

2n frequency dividing

Pulse width

Output circuit

Power source

Purchase specifications

